IN THE CLAIMS

Claim 1 (previously presented) A first multiple power sources control system, switching an originally used power source having an abnormal phenomenon to normal power sources to make loads keep obtaining supplied power and providing power calculation and load management to make a plurality of power sources in parallel connection to get larger power output, comprising:

a plurality of power input sides used to receive a plurality of external independent power sources;

a plurality of first voltage and current detecting modules used to detect whether said power sources of said power input sides is abnormal;

a plurality of power output sides used to supply a plurality of loads with required power;

a plurality of second voltage and current detecting modules used to detect whether power of said power output sides is abnormal;

a plurality of power source switching modules used to switch a power source supplying said power output sides with power due to a signal of a control module; and

said control module controlling a plurality of control switches to be in an ON or OFF state and controlling said power source switching modules according to a state informed from said first voltage and current detecting modules and said second voltage and current detecting module and can output a harmonic signal to said power input sides.

Claim 2 (previously presented) The multiple power sources control system as recited in claim 1, wherein at least two said power input sides receive the plurality of said external independent power sources.

Claim 3 (previously presented) The multiple power sources control system as recited in claim 1, wherein at least one said power output side supplies at least one of said loads with power.

Claim 4 (previously presented) The multiple power sources control system as recited in claim 1, wherein at least one first voltage and current detecting module detects at least two of said power input sides.

Claim 5 (previously presented) The multiple power sources control system as recited in claim 1, wherein at least one second voltage and current system detects at least one of said power output sides.

Claim 6 (previously presented) The multiple power sources control system as recited in claim 1, wherein said power source switching modules switches at least two of said plurality of independent power sources.

Claim 7 (currently amended) **[[A]]** **The** multiple power sources control system as recited in claim 1, wherein at least one of said power source switching modules are provided.

Claim 8 (currently amended) **The** ~~A first~~ **multiple** power ~~source~~ **sources** control **system as recited in claim 1 further** ~~module,~~ comprising:

a MOSFET transistor to control whether power is transmitted to **at least one of the plurality of loads** ~~a load~~;

a bridge rectifier to rectify power to provide said MOSFET transistor with an electric current;

a bias circuit to provide said MOSFET transistor a fixed bias; and

a coupler to control a state of said MOSFET transistor by an external control signal passing through said coupler.

Claim 9 (currently amended) The **[[first]]** **multiple** power ~~source~~ **sources** control ~~module~~ **system** as recited in claim 8, wherein said MOSFET transistor is replaced by other transistor components.

Claim 10 (currently amended) The **[[first]]** **multiple** power ~~source~~ **sources** control ~~module~~ **system** as recited in claim 8, wherein said coupler is replaced by other transistor components.

Claim 11 (currently amended) The **[[first]]** **multiple** power ~~source~~ **sources** control ~~module~~ **system** as recited in claim 8, wherein said bias circuit comprises:

a first transistor D1 used to rectify power;

a first resistance R1;

a first capacitance C1 proceeding first-stage voltage decay and filtering wave of power rectified by said first transistor D1 in coordination with said first resistance R1;

a second resistance R2;

a second capacitance C2 proceeding second-stage voltage decay and filtering wave of power processed by said first resistance R1 and said first capacitance C1 in coordination of said second resistance R2;

a third resistance R3; and

a Zener transistor D2 processing power processed by said second resistance R2 and said second capacitance C2 in coordination with said third resistance R3.

Claim 12 (currently amended) The **[[first]]** **multiple** power ~~source~~ **sources** control ~~module~~ **system** as recited in claim 11, wherein a third transistor D3 is added into said bias circuit to work in coordination with said first transistor D1.

Claim 13 (previously presented) A second power source control module, comprising:

a first MOSFET transistor;

a second MOSFET transistor;

a first diode;

a second diode;

a bias circuit providing said first MOSFET transistor and said second MOSFET transistor with a fixed bias; and

a coupler controlling states of said first MOSFET transistor, said second MOSFET transistor, said first diode, and said second diode by an external control signal passing through said coupler.

Claim 14 (previously presented) The second power source control module as recited in claim 13, wherein said MOSFET transistor is replaced with IGBT or other power components which can be turned on or off immediately.

Claim 15 (previously presented) The second power source control module as recited in claim 13, wherein said coupler is replaced with other transistor components.

Claim 16 (previously presented) The second power source control module as recited in claim 13, wherein said bias circuit is replaced with other transistor components.

Claim 17 (previously presented) The second power source control module as recited in claim 13, wherein said bias circuit comprises:

a first transistor D1 used to rectify power;

a first resistance R1;

a first capacitance C1 used to proceed first-stage voltage decay and filtering wave of power rectified by and said first transistor D1 in coordination with said first resistance R1;

a second resistance R2;

a second capacitance C2 used to proceed second-stage voltage decay and filtering wave of power processed by said first resistance R1 and said first capacitance C1 in coordination with said second resistance R2;

a third resistance R3; and

a Zener diode D2 used to process power processed by said second resistance R2 and said second capacitance C2 in coordination with said third resistance R3.

Claim 18 (previously presented) The second power source control module as recited in claim 17, wherein a second diode D2 is added into said bias circuit to work in coordination with said Zener diode D1 to rectify.